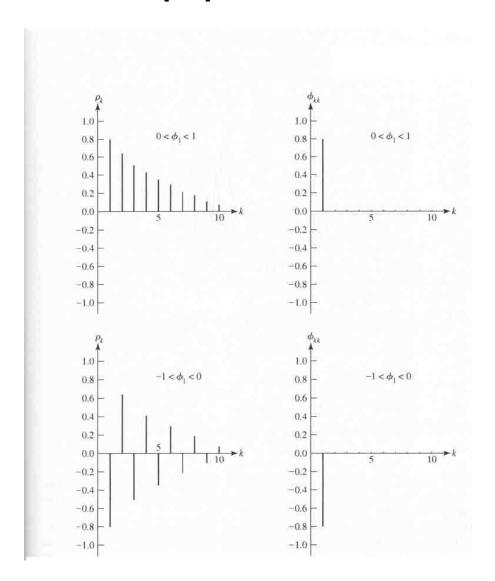
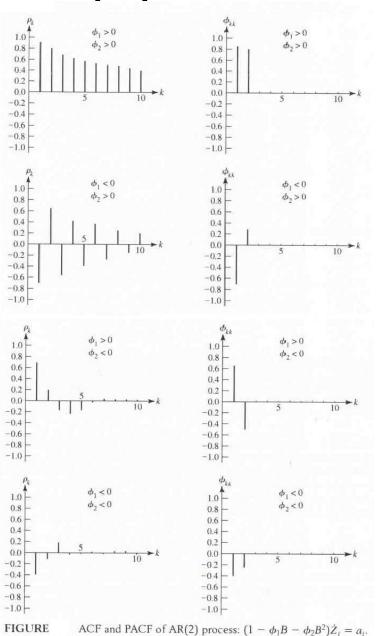
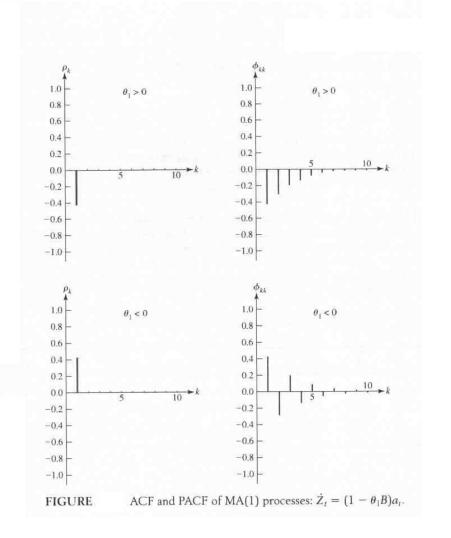
AR(1) PROCESS



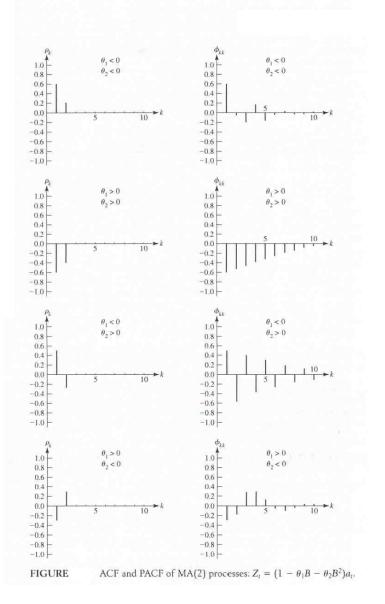
AR(2) PROCESS



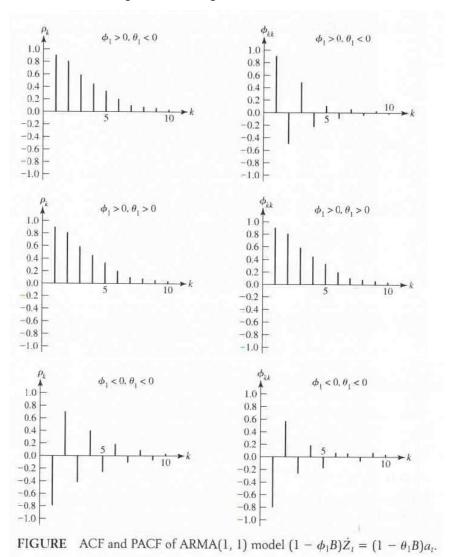
MA(1) PROCESS



MA(2) PROCESS



ARMA(1,1) PROCESS



ARMA(1,1) PROCESS (contd.)

